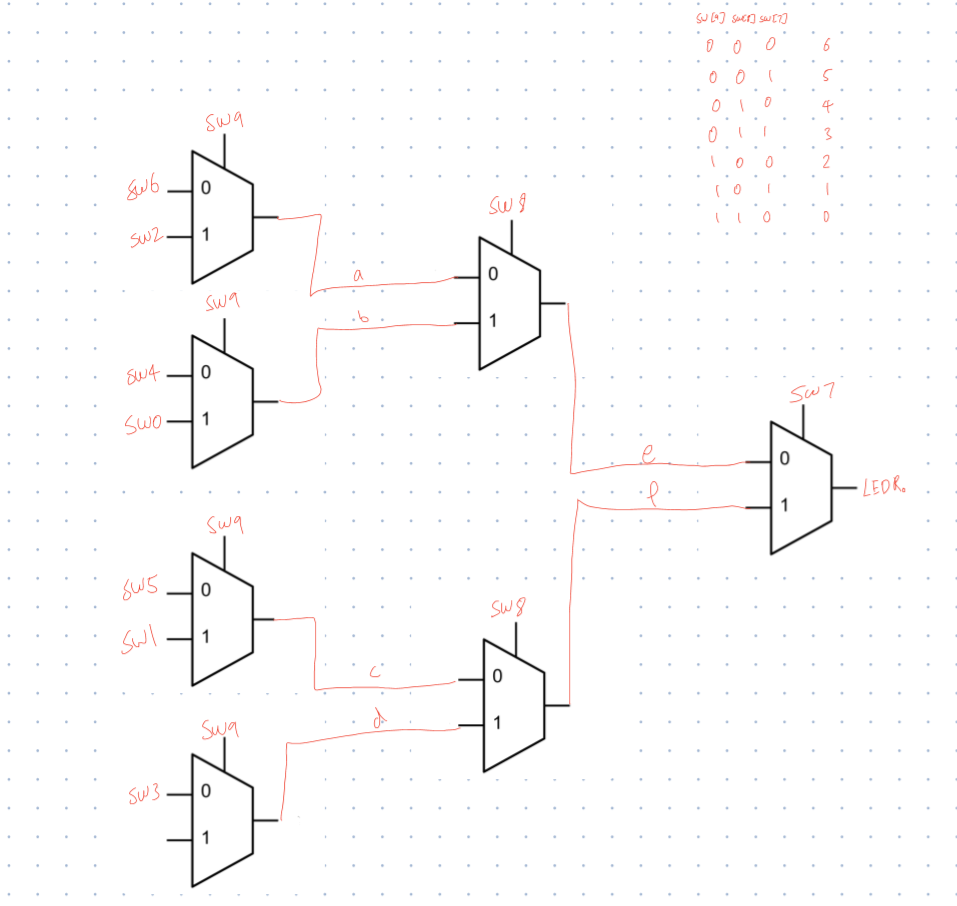
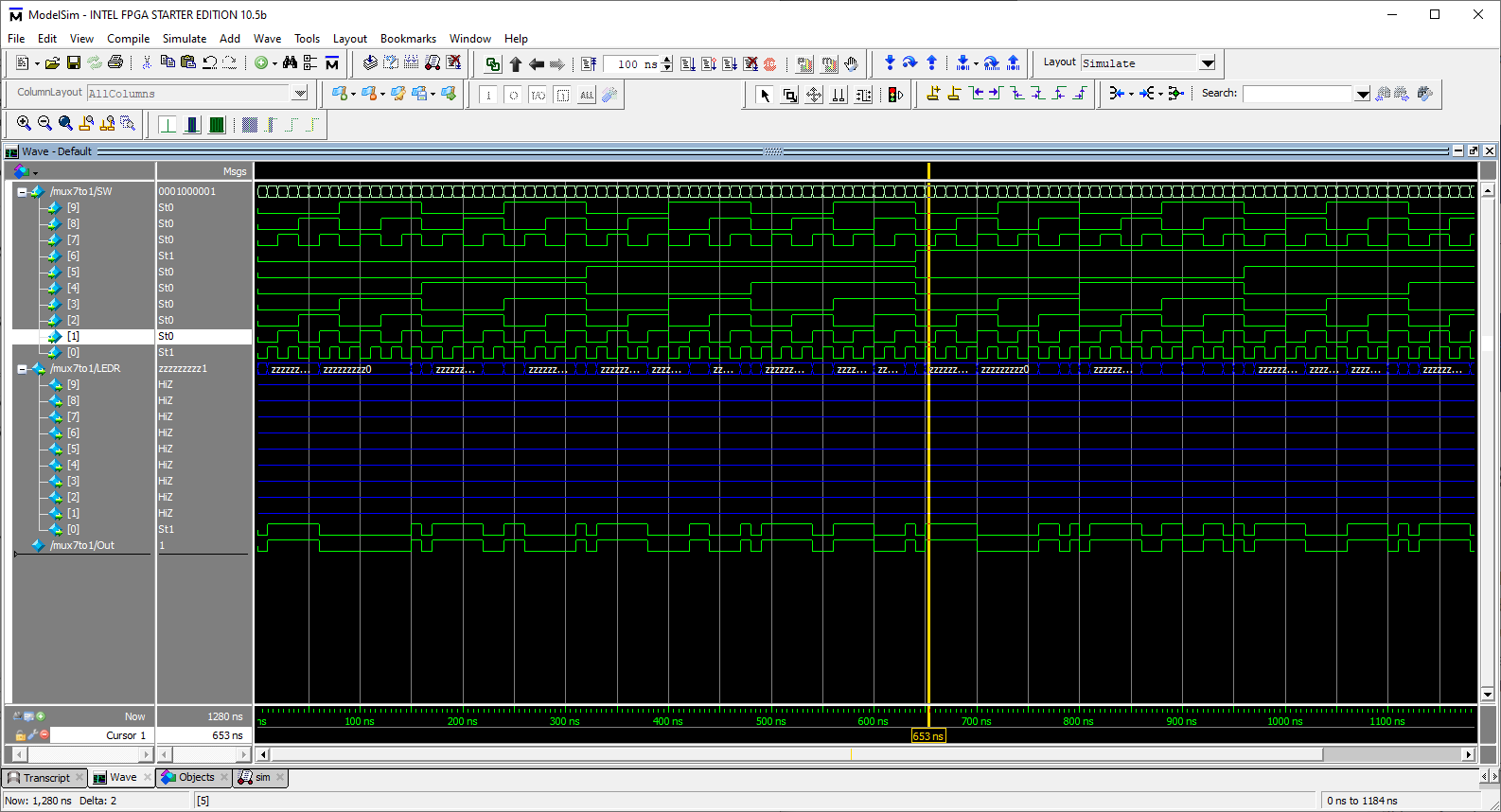
Lab 3 prelab

Part I:

1. 

Schematic for 7to1 multiplexer

1. Submitted mux7to1.v on Quercus
2. 

Simulation of 7to1 multiplexer.